

# Indian Institute of Information Technology Tiruchirappalli



इलेक्ट्रॉनिक्स एवं  
सूचना प्रौद्योगिकी मंत्रालय  
MINISTRY OF  
ELECTRONICS AND  
INFORMATION TECHNOLOGY



सी डैक  
CDAC



Chips to Startup  
Programme

## Chips to Startup Programme (C2S)

**Special Manpower Development Program for Chips to System Design (SMDP-C2SD)**

**Category-II: Development of Application Oriented Working Prototype of IPs/ASICs/SoCs**

**Project Title: Ultra Low Power SHAKTI RISC V Based Lightweight Edge AI Processor for IoT enabled Healthcare Applications**

### Software Details

S.No	Software Description
1	Cadence tools
2	Siemens tools
3	Synopsys tools
4	Ansys tools
5	Xilinx-AMD (Vivado, Vitis) tools

**Note: Software sponsored by MeitY in association with CDAC**