

Indian Institute of Information Technology Tiruchirappalli



इलेक्ट्रॉनिक्स एवं
सूचना प्रौद्योगिकी मंत्रालय
MINISTRY OF
ELECTRONICS AND
INFORMATION TECHNOLOGY



सी डैक
CDAC



Chips to Startup
Programme

Chips to Startup Programme (C2S)

Special Manpower Development Program for Chips to System Design (SMDP-C2SD)
Category-II: Development of Application Oriented Working Prototype of IPs/ASICs/SoCs

Project Title: Ultra Low Power SHAKTI RISC V Based Lightweight Edge AI Processor for IoT enabled Healthcare Applications

Project Investigators

Chief Investigator



Prof. G. Seetharaman,
Chief investigator,
Professor,
Department of Electronics and Communication Engineering,
Indian Institute of Information Technology Tiruchirappalli

Thrust Areas of Research: Semiconductor nanostructure and Optoelectronics Device Fabrication, ID Metal oxide Semiconductor Based Photo detector and Solar cell, ASIC and FPGA, Network on Chip, Wireless Transceivers, VLSI Design and Signal Processing

Co-Chief Investigator

Dr. R. Dhanalakshmi,
Co-Chief investigator,
Associate Professor,
Department of Computer Science and Engineering,
Indian Institute of Information Technology Tiruchirappalli



Thrust Areas of Research: Data Analytics, Recommender Systems, WSN, Machine Learning, Computer Networks, Combinatorial Optimization Techniques