

Indian Institute of Information Technology Tiruchirappalli



इलेक्ट्रॉनिक्स एवं
सूचना प्रौद्योगिकी मंत्रालय
MINISTRY OF
ELECTRONICS AND
INFORMATION TECHNOLOGY



सी डैक
CDAC



Chips to Startup
Programme

Chips to Startup Programme (C2S)

Project Details

Special Manpower Development Programme for Chips to System Design (SMDP-C2SD)

Category-II: *Development of Application Oriented Working Prototype of IPs/ASICs/SoCs*

Project Title	Ultra Low Power SHAKTI RISC V Based Lightweight Edge AI Processor for IoT enabled Healthcare Applications
Project Duration	2024 – 2029 (5 years)
Funding agency	Ministry of Electronics & Information Technology (MeitY), New Delhi-110003
Nodal Institute	National Institute of Technology Tiruchirappalli
Co-Participating Institute	National Institute of Electronics & Information Technology (NIELIT), Aurangabad
Total Project Outlay	Rs. 2.88/- Crore
IIIT Budget Outlay	Rs. 96/- Lakh